

CS Masters' Thesis Defense

Title: *Enhancement of MC68000 Simulator Macros to Support Floating Point Number*
Speaker: Honey Walia
Date: Tuesday, March 20, 2012
Time: 2:00 p.m.
Location: GMCS 405
Thesis advisor: Dr Leland Beck

Abstract:

The purpose of this thesis is to write macros to support floating point number for BSVC simulator. This simulator provides a runtime environment which simulates a Motorola 68000 family CPU system. This system is currently used at SDSU to teach machine organization and assembly language programming to students in CS237. This thesis provides significant enhancements to the functionality of the system, making it more valuable tool for classroom instruction.